Amendments to the Specification

Please amend the specification as follows.

Page 1, after the title, insert the following:

Cross Reference to Related Applications

This application is a continuation application that claims priority to U.S. Application Serial No. 09/471,202 (filed December 23, 1999) (allowed February 20, 2004), the entire contents of which are incorporated herein.

On page 5, under DETAILED DESCRIPTION, please replace the first paragraph with the following:

An embodiment of the present invention is directed to a single array banked branch target buffer. Referring now in detail to the drawings wherein like parts are designated by like reference numerals throughout, FIG. 3 illustrates a four-bank branch target buffer according to an embodiment of the present invention. A lookup IP signal passes through a multiplexer 150 coupled to four buffer entries 250, or "ways." Note that the number of ways (four) does not have to be the same as the number of banks in the branch target buffer. The buffer entries 250 are mapped to cache data buffers 350, and each buffer entry 250 is coupled to four comparators 450. The lookup IP signal is also input to the comparators 450 together with a hard-wired bank number (i.e., "00," "01," "10" and "11"), and the results from each comparator ~~400~~450 are coupled to multiplexers 550 that select cache data stored in the cache data buffers 350. As a result, information for four banks is selected.